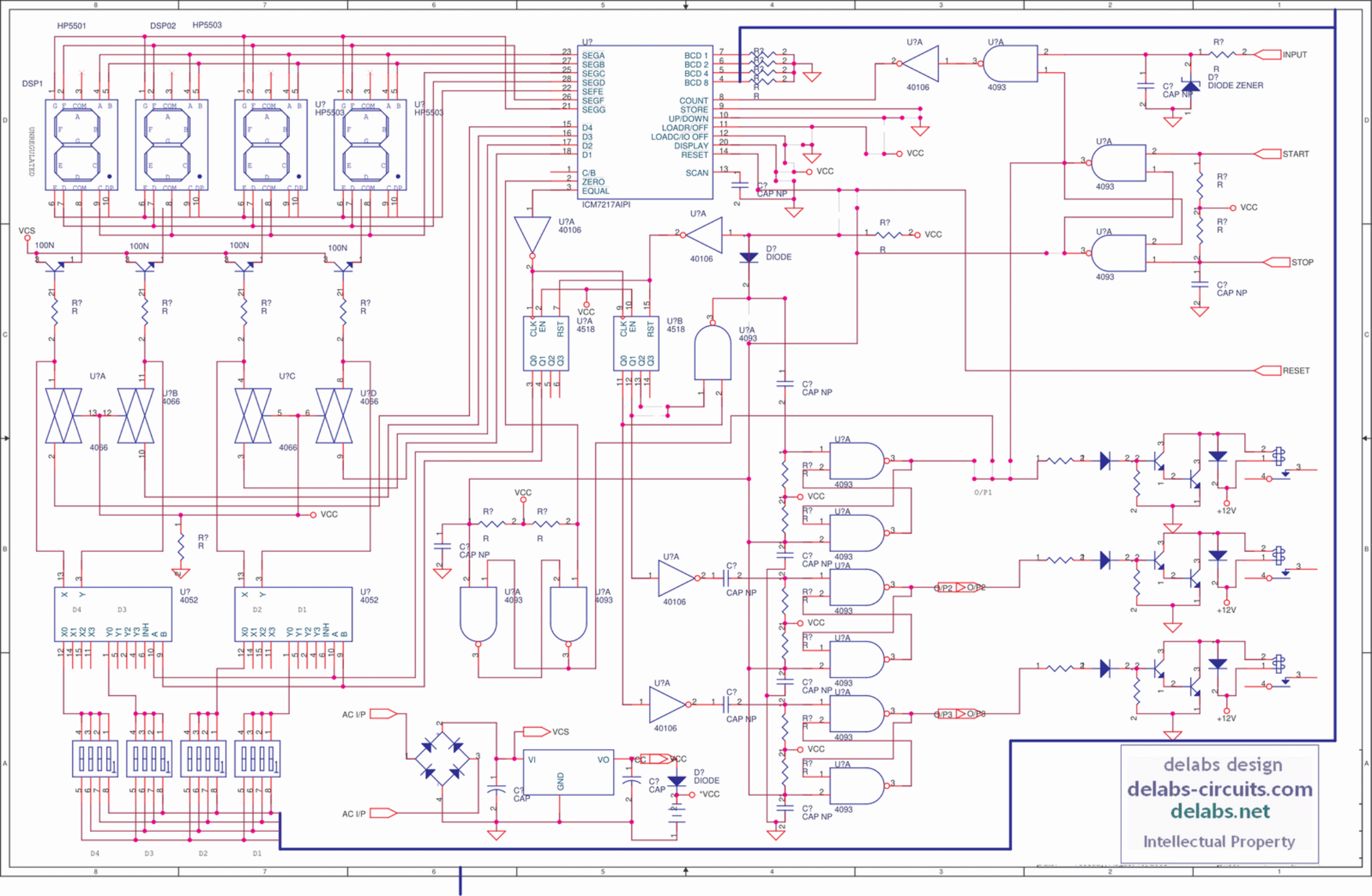
HP5501
DSP02
HP5503
DSP1
UNREGULATED
U?
HP5503
VCS
100N
R?
R
U?A
U?B
U?C
U?D
4066
U?
4052
D4
D3
D2
D1
U?
SEGA
SEGB
SEGC
SEGD
SEFE
SEGF
SEGG
D4
D3
D2
D1
C/B
ZERO
EQUAL
BCD 1
BCD 2
BCD 4
BCD 8
COUNT
STORE
UP/DOWN
LOADR/OFF
LOADC/IO OFF
DISPLAY
RESET
SCAN
ICM7217AIPI
U?A
40106
U?A
4093
U?A
4518
U?B
4518
CLK
EN
RST
Q0
Q1
Q2
Q3
D?
DIODE
C?
CAP NP
VCC
INPUT
START
STOP
RESET
D?
DIODE ZENER
O/P1
O/P2
O/P3
+12V
AC I/P
VCS
VI
VO
GND
C?
CAP
D?
DIODE
*VCC
delabs design
delabs-circuits.com
delabs.net
Intellectual Property